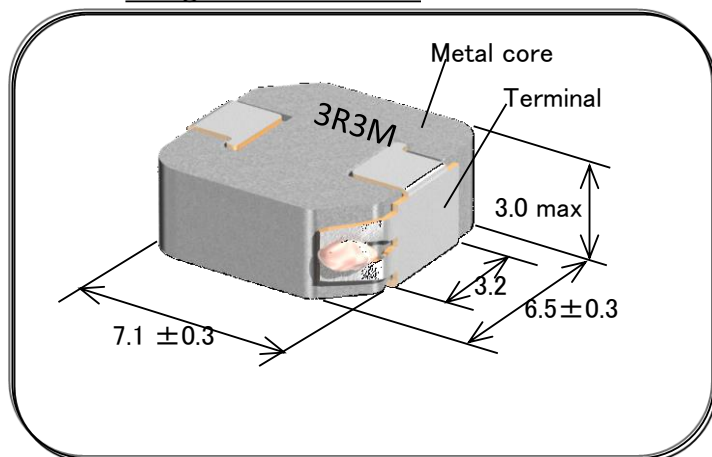


**Component
Image & Dimension**

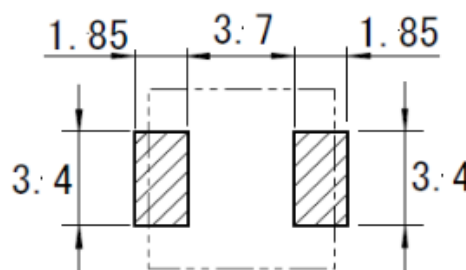


Features :

- a) Small Footprint and Low Profile Design :
Footprint : 7.1 x 6.5 mm Typ.
Height : 3.0mm Max.
- b) High Power Handling Capability :
Small Copper Loss
Using Large Saturation Induction of Fe- based metals
- c) Flat inductance performance over temperature
based on the high curie temperature of the

Applications :
Note Book & Mobile Computer,
VRM, etc.

Recommended pad layout :



Electrical Specification

TDK Identification	Inductance		Test Freq. (kHz)	DC Resistance		Rated DC Current	
	at 0A (uH)	Tol. (%)		Spec. (m-Ohm)	Typ. (m-Ohm)	Idc 1 (A) typ	Idc 2 (A) typ.
SPM6530T- R25M230	0.25	+/-20%	100	3.08 max	2.8	28.3	23.0
SPM6530T- R47M170	0.47	+/-20%	100	3.96 max	3.6	20.5	20.0
SPM6530T- R68M140	0.68	+/-20%	100	5.06 max	4.6	16.6	16.0
SPM6530T- 1R0M120	1.00	+/-20%	100	7.92 max	7.2	14.1	13.0
SPM6530T- 1R5M100	1.50	+/-20%	100	11.11 max	10.1	11.5	11.0
SPM6530T- 2R2M	2.20	+/-20%	100	19.03 max	17.3	8.4	8.2
SPM6530T- 3R3M	3.30	+/-20%	100	29.70 max	27.0	7.3	6.8
SPM6530T- 4R7M	4.70	+/-20%	100	39.38 max	35.8	6.2	5.6
※ SPM6530T- 5R6M	5.60	+/-20%	100	41.80 max	38.0	5.0	5.6
※ SPM6530T- 6R8M	6.80	+/-20%	100	53.24 max	48.4	4.3	4.9
※ SPM6530T- 8R2M	8.20	+/-20%	100	66.88 max	60.8	4.1	4.4
※ SPM6530T- 100M	10.00	+/-20%	100	72.49 max	65.9	3.7	4.2
※ SPM6530T- 150M	15.00	+/-20%	100	115.28 max	104.8	3.0	3.3
※ SPM6530T- 220M	22.00	+/-20%	100	171.27 max	155.7	2.5	2.7

Note. Idc 1 : Based on the inductance change.(drop 20% typ.)

Idc 2 : Based on the self temperature rise. (+40 deg typ.)

Operating Temperature Range: -40 °C~+125 °C (including self temperature rise)

※ Sampling Stage

※ Simulation Data

Caution: Please contact our sales person when you consider organic solvent or aqueous cleaning.